

Single Driver for GaAs FET Switches and Attenuators

M/A-COM Products Rev. 4

Features

- High Speed CMOS Technology
- **Complementary Outputs**
- Positive Voltage Control
- Low Power Dissipation
- Plastic SOIC Package for SMT Applications
- Tape and Reel Packaging Available
- SOIC-8 Package

Description

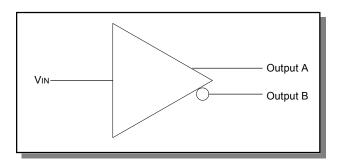
M/A-COM's DR65-0109 is a Single channel driver used to translate TTL control inputs into complementary gate voltages for GaAs FET microwave switches and attenuators. High speed analog CMOS technology is utilized to achieve low power dissipation at moderate to high speeds. encompassing most microwave switching applications.

Ordering Information

Part Number	Package			
DR65-0109	Bulk Packaging			
DR65-0109TR	1000 piece reel			

Note: Reference Application Note M513 for reel size information

Logic Diagram



Pin Configuration

Pin No.	Function			
1	V _{cc}			
2	V _{IN}			
3	GND			
4	GND			
5	GND			
6	Output A			
7	Output B			
8	V _{EE}			

Guaranteed Operating Ranges

Symbol	Parameter ¹	Unit	Min	Typical	Max
VCC	Positive DC Supply Voltage	V	4.5	5.0	5.5
V _{EE}	Negative DC Supply Voltage	V	-5.5	-5.0	-4.5
V _{CC-} V _{EE}	V _{CC-} V _{EE} Positive to Negative Supply Range		9.0	10.0	11.0
T _A	T _A Operating Ambient Temperature		-40	+25	+85
I _{OH}	DC Output Current - HIGH	mA	_	_	-1.0
I _{OL}	I _{OL} DC Output Current - LOW		_	_	1.0
T_{rise},T_{fall}	Maximum Input Rise or Fall Time	nS	_	_	500

1. All voltages are relative to GND

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- Europe Tel: 44.1908.574.200 / Fax: 44.1908.574.300
- Asia/Pacific Tel: 81.44.844.8296 / Fax: 81.44.844.8298 Visit www.macom.com for additional data sheets and product information.



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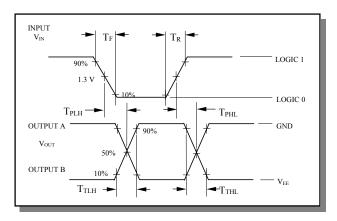
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AC & DC Characteristics Over Guaranteed Operating Range

Symbol	Parameter	Test Conditions		Units	Min	Тур	Max
VIH	Input HIGH Voltage	Guaranteed HIGH Input Voltage		V	2.0	_	-
V _{IL}	Input LOW Voltage	Guaranteed LOW Input Voltage		V	-	_	0.8
V _{OH}	Output HIGH Voltage	I _{OH} = -1 mA	V _{EE} = Max	V	- 0.1	_	-
V _{OL}	Output LOW Voltage	I _{OL} = 1 mA	V _{EE} = Max	V	_	_	V _{EE} + 0.1
I _{IN}	Input Leakage Current	V _{IN} = V _{CC} or GND	V _{EE} = Min	μA	-1.0	0	1.0
I _{cc}	Quiescent Supply Current	V _{CC} = Max	$V_{EE} = Min$ $V_{IN} = V_{CC} \text{ or GND}$	μA	_	_	400
T _{PHL} , T _{PLH}	Propagation Delay	Guaranteed -40° C to + 85° C		nS	_	_	50
T _{THL} , T _{TLH}	Output Transition Time	Guaranteed -40° C to + 85° C		nS	_	_	25
	Delay Skew, Output A to Output B	Guaranteed -40° C to + 85° C		nS	_	_	8

See Switching Wave Forms for the definition of the switching terms. Supplies must be by-passed with .01 μF Capacitors.

Switching Waveforms



Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Silicon Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Absolute Maximum Ratings^{2,3}

Parameter	Absolute Maximum		
V _{CC}	5V to + 6.0 V		
V _{EE}	- 6.0 V to5 V		
V _{CC} - V _{EE}	12 V		
V _{IN} ⁴	V _{CC} + .5 V		
V _{OUT}	V _{EE} 5 V		
Storage Temperature	-65°C to +150°C		

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- M/A-COM does not recommend sustained operation near these survivability limits.
- Standard CMOS TTL interface, latch-up will occur if logic signal is applied prior to power supply.

Truth Table

Input	Outputs		
V _{IN}	Α	В	
0	V _{EE}	GND	
1	GND	V _{EE}	

PRELIMINARY: Data Sheets contain information regarding a product M/A-COM has under development. Performance is based on engineering tests. Specifications are typical. Mechanical outline has been fixed. Engineering samples and/or test data may be available. Commitment to produce in volume is not quaranteed.

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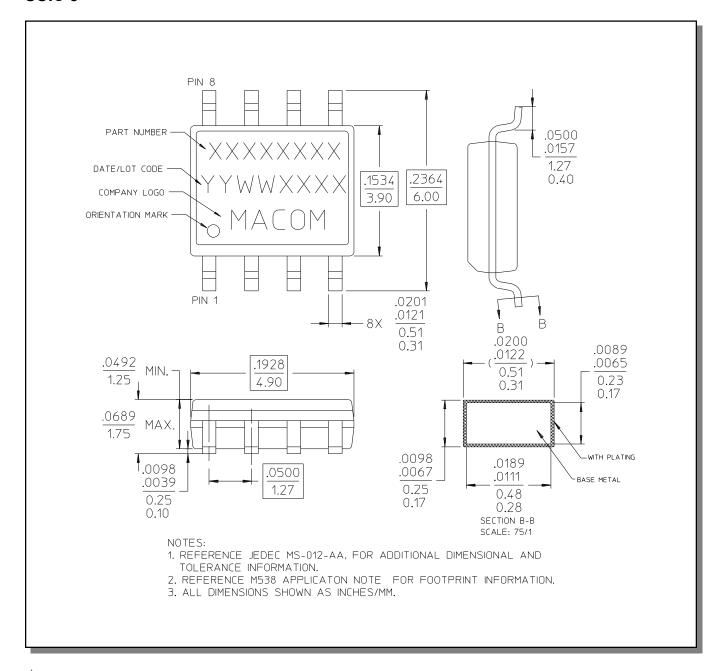
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SOIC-8[†]



[†] Reference Application Note M538 for lead-free solder reflow recommendations.

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